



Welcome United States Patent and Trademark Office

Search Results**BROWSE****SEARCH****IEEE Xplore Guide****SUPPORT**

Results for "((microcontroller or controller<in>metadata) <and> (mode<in>metadata))<a..."

Your search matched 4 of 1237766 documents.

A maximum of 100 results are displayed, 25 to a page, sorted by **Relevance in Descending** order.
 e-mail printer friendly
» [Search Options](#)[View Session History](#)[Modify Search](#)[New Search](#)

((microcontroller or controller<in>metadata) <and> (mode<in>metadata))<a...>


 Check to search only within this results set
» **Key**Display Format: Citation Citation & Abstract

IEEE JNL IEEE Journal or Magazine

IEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard

Select Article Information

1. A 9-M tr. access network system-on-a-chip for mega-bit Internet access at home

Kozu, S.; Aramaki, T.; Ikeda, C.; Kuroda, Y.; Kawanago, S.; Okada, M.; Kariya, H.; Manabe, M.; Utani, H.; Sudou, E.; Oda, Y.; Suzuki, H.;

Custom Integrated Circuits Conference, 2000. CICC. Proceedings of the IEEE 2000

21-24 May 2000 Page(s):231 - 234

Digital Object Identifier 10.1109/CICC.2000.852655

[AbstractPlus](#) | Full Text: [PDF](#)(328 KB) [IEEE CNF](#)**2. A single chip terminal solution for high-end telephone applications**

Nair, V.; Erdmann, M.; Mishra, S.; Povazanec, J.; Shaligram, A.; Hu Chun Feng; .

Custom Integrated Circuits, 2001, IEEE Conference on.

6-9 May 2001 Page(s):417 - 420

Digital Object Identifier 10.1109/CICC.2001.929813

[AbstractPlus](#) | Full Text: [PDF](#)(628 KB) [IEEE CNF](#)**3. Implementation of the USB interface in the instrumentation for sound and vibration measurements and analysis**

Podgorski, A.; Nedwidek, R.; Pochmara, M.;

Intelligent Data Acquisition and Advanced Computing Systems: Technology and Applications, 2003. Proceedings of the Second IEEE International Workshop on

2003 Page(s):159 - 163

Digital Object Identifier 10.1109/IDAACS.2003.1249539

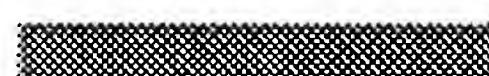
[AbstractPlus](#) | Full Text: [PDF](#)(481 KB) [IEEE CNF](#)**4. A mixed-mode ASIC for interface control of smart-card parcmeter**

Compagne, E.; Ilie, F.;

Euro ASIC '91

27-31 May 1991 Page(s):124 - 127

Digital Object Identifier 10.1109/EUASIC.1991.212881

[AbstractPlus](#) | Full Text: [PDF](#)(300 KB) [IEEE CNF](#)

Freeform Search

Database: US Pre-Grant Publication Full-Text Database
 US Patents Full-Text Database
 US OCR Full-Text Database
 EPO Abstracts Database
 JPO Abstracts Database
 Derwent World Patents Index
 IBM Technical Disclosure Bulletins

Term:

Display: Documents in Display Format: CIT Starting with Number

Generate: Hit List Hit Count Side by Side Image

Search **Clear** **Interrupt**

Search History

DATE: Wednesday, September 21, 2005 [Printable Copy](#) [Create Case](#)

Set Name **Query**

side by side

DB=PGPB; PLUR=YES; OP=OR

L3 20030093606 or 20030079077

2 L3

L2 20030145141

1 L2

DB=USPT; PLUR=YES; OP=OR

L1 6567273.pn. or 5773332.pn. or 6438638.pn. or 6372974.pn.

4 L1

Hit Count **Set Name**

result set

END OF SEARCH HISTORY

Refine Search

Search Results -

Terms	Documents
L1 same (serial adj1 bus) same (host or CPU or computer)	21

Database:

- US Pre-Grant Publication Full-Text Database
- US Patents Full-Text Database
- US OCR Full-Text Database
- EPO Abstracts Database
- JPO Abstracts Database
- Derwent World Patents Index
- IBM Technical Disclosure Bulletins

Search:

Refine Search
Recall Text
Clear
Interrupt

Search History

DATE: Wednesday, September 21, 2005 [Printable Copy](#) [Create Case](#)

<u>Set</u> <u>Name</u>	<u>Query</u>	<u>Hit</u> <u>Count</u>	<u>Set</u> <u>Name</u>
side by side			result set
DB=PGPB,USPT,USOC; PLUR=YES; OP=OR			
<u>L2</u>	L1 same (serial adj1 bus) same (host or CPU or computer)	21	<u>L2</u>
<u>L1</u>	((dual adj1 mode) or mode) near3 (microcontroller or (micro adj1 controller) or controller)	25887	<u>L1</u>

END OF SEARCH HISTORY

Refine Search

Search Results -

Terms	Documents
L2	0

US Pre-Grant Publication Full-Text Database
 US Patents Full-Text Database
 US OCR Full-Text Database
Database: EPO Abstracts Database
 JPO Abstracts Database
 Derwent World Patents Index
 IBM Technical Disclosure Bulletins

Search: L3 Refine Search
 Clear Interrupt

Search History

DATE: Wednesday, September 21, 2005 [Printable Copy](#) [Create Case](#)

Set	Name	Query	Hit	Set
Name	Query		Count	Name
side by side			result set	
DB=EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR	L3	L2	0	L3
DB=PGPB,USPT,USOC; PLUR=YES; OP=OR	L2	L1 same (serial adj1 bus) same (host or CPU or computer)	21	L2
	L1	((dual adj1 mode) or mode) near3 (microcontroller or (micro adj1 controller) or controller)	25887	L1

END OF SEARCH HISTORY

EAST - [Untitled 1]

File View Edit Tools Window Help

Drafts Pending Active L1: (17811) ((dual adj1 mode) near3 (microcontroller or bus) same (host or CPU)) OR L2: (14) l1 same (serial adj1 mode) near3 (microcontroller or bus) same (host or CPU) L3: (21) l1 same (serial adj1 mode) near3 (microcontroller or bus) same (host or CPU)

Failed Saved Favorites Tagged (0) UDC Queue Trash

Search DSes USPA Default operator: OR

Plurals Highlight all hit items in balloons

BRSform SCRform Image Text HTML

	Type	L #	Hits	Search Text	DSes	Time	Stamp	Comment	Error	Definitive	Err
1	BRS	L1	17811	((dual adj1 mode) or mode) near3 (microcontroller or bus) same (host or CPU)	USPA	2005/09/21	17:16				
2	BRS	L2	14	l1 same (serial adj1 mode) near3 (microcontroller or bus) same (host or CPU)	USPA	2005/09/21	17:18				
3	BRS	L3	21	l1 same (serial adj1 mode) near3 (microcontroller or bus) same (host or CPU)	USPA	2005/09/21	17:18				

Start > EAST - [...]

EAST - [Untitled1.ed]

File View Tools Window Help

X

[] [] [] [] [] []

- Drafts
- Pending
- Active
 - L1: (17811) ((dual adj1))
 - L2: (14) 11 same (serial adj1)
 - L3: (21) 11 same (serial adj1)
- Failed
- Saved
- Favorites
- Tagged (0)
- UDC
- Queue
- Trash

Search:	...	<input type="button" value="Browse"/>	<input type="button" value="Close"/>	<input type="button" value="Clear"/>	
EBSS USPAT					<input checked="" type="checkbox"/> Starts
Default operator: OR					<input checked="" type="checkbox"/> highlight all hit items initially
<div style="border: 1px solid black; padding: 2px; width: 100%; height: 100%;"> 11 same (serial adj1 bus) same (host or CPU or computer or processor) </div>					

◀ ▶

BRFormat SCRFormat Image Text HTML

	U	I	Document ID	Issue Date	Pages	Title	Current OR	Current X
1	<input type="checkbox"/>	<input type="checkbox"/>	US 6820160 B1	20041116	13	Apparatus for optically isolating a USB peripheral device	710/305	710/36; 710/62
2	<input type="checkbox"/>	<input type="checkbox"/>	US 6813650 B1	20041102	12	Multimode non-standard universal serial bus connector	710/14	341/22; 345/173
3	<input type="checkbox"/>	<input type="checkbox"/>	US 6760333 B1	20040706	44	Hybrid digital subscriber loop and voice over IP	370/395.1	370/465; 375/222
4	<input type="checkbox"/>	<input type="checkbox"/>	US 6643728 B1	20031104	20	Method and apparatus for converting IEEE 1284 to USB	710/315	710/313
5	<input type="checkbox"/>	<input type="checkbox"/>	US 6567921 B1	20030520	7	Asynchronous low power mode bus controller circuit	713/322	713/601
6	<input type="checkbox"/>	<input type="checkbox"/>	US 6513085 B1	20030128	25	Link/transaction layer controller with integrated memory	710/305	370/438; 703/23
7	<input type="checkbox"/>	<input type="checkbox"/>	US 6505263 B1	20030107	21	Bus controller operating code in system	710/100	710/107; 710/305
8	<input type="checkbox"/>	<input type="checkbox"/>	US 6463362 B1	20021008	7	Electronic device and operating mode control	700/295	700/297; 700/298
9	<input type="checkbox"/>	<input type="checkbox"/>	US 6266715 B1	20010724	16	Universal serial bus controller with a direct connection to expansion card	710/22	709/212; 709/234
10	<input type="checkbox"/>	<input type="checkbox"/>	US 6209050 B1	20010327	11	Expansion card	710/301	710/105; 710/111
11	<input type="checkbox"/>	<input type="checkbox"/>	US 6122694 B1	20000919	8	Serial bus controller	710/110	710/242;

Start □ ▶ ■ > EAST - [...]